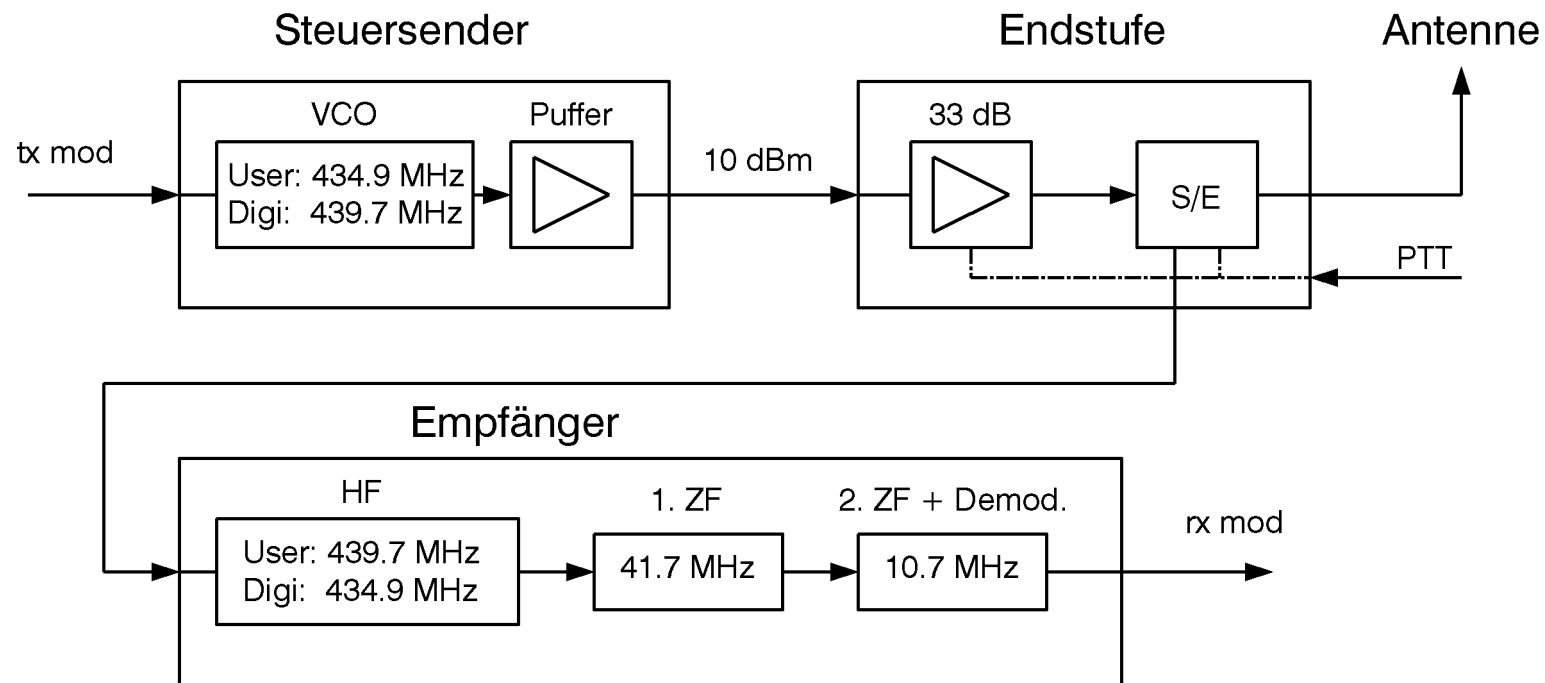


Breitbandiger Benutzerzugang im 70-cm-Band

Transceiver für Digi und User (nur für Duplex geeignet) von DL2ZBN und DL8AAU:



Empfängerbaugruppe:

```
graph LR; HF --> VV[\"VV AT41586\"]; VV --> Helix[\"Helix 510501\"]; Helix --> TDA5331[\"VCO, Mischer, SAW-Treiber TDA5331\"]; TDA5331 --> Filter[\"Filter TFS41\"]; Filter --> SA626[\"XO, Mischer, ZF-Verst., Demod. SA626\"]; SA626 --> Puffer[\"Puffer BC546\"]; Puffer --> rx_mod[\"rx mod\"]; SA626 --> OP[\"OP LM358\"]; OP --> RSSI[\"RSSI\"]; OP --> DCD[\"DCD\"]; SA626 <--> Quarz[\"Quarz 52.4 MHz\"]; SA626 <--> Filter2[\"Filter 2 * SFE10.7M3\"]; TDA5331 <--> PLL[\"PLL SDA3302-5\"]; PLL <--> I2C[\"I²C-Bus PIC12C508\"]; I2C -- User/Digi --> UserDigi[\"User/Digi\"]; I2C <--> SA626;
```

Senderbaugruppe:

```
graph LR; tx_mod[\"tx mod\"] --> VCO[\"VCO BF979\"]; VCO --> Puffer1[\"Puffer BF960\"]; Puffer1 --> Damp1[\"Dämpf. 7dB\"]; Damp1 --> Puffer2[\"Puffer BFR90\"]; Puffer2 --> Damp2[\"Dämpf. 7dB\"]; Damp2 --> Puffer3[\"Puffer BFR90\"]; Puffer3 --> Out[\"\"]; VCO <--> PLL[\"PLL SDA3302-5\"]; PLL <--> I2C[\"I²C-Bus PIC12C508\"]; I2C -- User/Digi --> UserDigi[\"User/Digi\"]; I2C <--> Puffer1;
```